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S/N 10/681,509

DEC 2 1 2005

PATENT

## <u>IN THE UNITED STATES PATENT AND TRADEMARK OFFICE</u>

Applicant: Clceves, et al.

Examiner:

Ginette Peralta

Serial No.: 10/681,509

Group Art Unit: 2814

Filed:

Docket:

MA-108

Title:

October 7, 2003

Uniform Seeding to Control Grain and Defect Density of Crystallized Silicon for

Use in Sub-Micron Thin Film Transistors

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §1.97 et. seq., the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(e)(2) I hereby state that no item of information in the Information Disclosure Statement filed herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than 3 months prior to the filing of this Information Disclosure Statement.

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's belowsigned representative. Applicant acknowledges the requirement to submit copies of foreign

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Page 2 Dkt: MA-108

Script No :10/681,509

Title: Uniform Seeding to Control Grain and Defect Density of Crystallized Silicon for Use in Sub-Micron Thin Pilm Transistors

patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Tamela J. Squyres, Reg. No.52246

Matrix Semiconductor, Inc.

3230 Scott Blvd

Santa Clara, CA 95054

Date: December 21, 2005

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-

1450, on this 7 ( ) day of December, 2005.

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use es marly shoets as nocassery)	Application Number	10/681,509
	Filing Date	October 7, 2003
	First Named Inventor	Cleeves, James
	Group Art Unit	2814
	Examiner Name	Peralta, Ginette
	Attorney Docket No: I	MA-108

Examiner	Cito	USP Document Number	Bublishedon Date	T DOCUMENTS  Name of Patentse or  Applicant of cited Document	Class	\$ubclass	Filing Dato
initial *	A1	6,346,437	02/12/2002	Maekawa et al.	438	166	

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		Name of Patentee or Applicant of	Class	Subclass	45
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Initials* No					

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EXAMINER

DATE CONSIDERED

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